

Appl. No. 09/921,471

In the Claims

Please amend the Claims as follows:

1-7. (cancelled)

8. (currently amended) A method of receiving a digital  
5 signal, comprising:  
comparing said digital signal to a reference voltage;  
determining when said digital signal has changed from  
June 3, 2004 being greater than said reference voltage to being  
less than said reference voltage; and,  
10 reducing said reference voltage after said digital signal  
has changed from being greater than said reference voltage to  
being less than said reference voltage, wherein said reference  
voltage is reduced from a highest reference voltage to a  
lowest reference voltage through a plurality of intermediate  
15 reference voltages during a single half-cycle of an input said  
digital signal by selecting different nodes on a resistive  
ladder to provide said reference voltage.

9. (original) The method of claim 8 wherein said reference  
20 voltage is reduced over a period of time that is greater than  
an expected period of time for said digital signal to change  
from one digital state to another.

10. (currently amended) A method of receiving a digital  
signal, comprising:  
25 comparing said digital signal to a reference voltage;  
determining when said digital signal has changed from  
being less than said reference voltage to being greater than  
said reference voltage; and,  
increasing said reference voltage after said digital

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signal has changed from being less than said reference voltage to being greater than said reference voltage, wherein said reference voltage is increased from a lowest reference voltage to a highest reference voltage through a plurality of intermediate reference voltages during a single half-cycle of an input said digital signal by selecting different nodes on a resistive ladder to provide said reference voltage.

11. (original) The method of claim 10 wherein said reference voltage is increased over a period of time that is greater than an expected period of time for said digital signal to change from one digital state to another.

12. (currently amended) A method, comprising:

adjusting a reference between a first nominal reference level and a second nominal reference level through a plurality of intermediate reference levels during a single half-cycle of an input signal by selecting among a plurality of nodes on a resistive ladder as a source for said reference;

adjusting said reference between said second nominal reference level and said first nominal reference level through said plurality of intermediate reference levels during another half-cycle of said input signal by selecting among said plurality of nodes on said resistive ladder as a source for said reference;

comparing ~~a~~ said input signal to said first nominal reference level when said input signal is closer to said first nominal reference level than said second nominal reference level; and,

comparing said input signal to said second nominal reference level when said input signal is closer to said second nominal reference level than said first nominal reference level.

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13. (currently amended) The method of claim 12 wherein said steps of comparing are used to initiate said steps of adjusting so that said reference becomes closer to said first nominal reference level after said input signal has crossed said second nominal reference level and so that said reference becomes closer to said second nominal reference level after said input signal has crossed said first nominal reference level.

14-22. (cancelled)